# Accelerating Convolutional Neural Networks Using Low Precision Arithmetic

## Hiroki Naganuma, Rio Yokota Tokyo Institute of Technology

#### Background of our research

- Convolutional Neural Network(CNN)
- 1. Performance overwhelming traditional methods in the field of image recognition
- 2. Tendency to **multilayer** as accuracy improves
- 3. Increase in calculation and data volume related to training (Training) and inference (Inference)
- 4. Bottleneck is calculated by **convolution**

25.80%

28.20%

Reducing calculation / data volume is a issue

By using depth learning (CNN) Significant decrease in error rate

#### Research concept

#### low precision data type and arithmetic

 Verify and evaluate a method to compress the amount of data contained in CNN and speed up Parallelize using SIMD instruction

Acceleration method to maintain high recognition accuracy Application to large data set and recognition accuracy float32(general) **16-bit Floating-Point** 8-bit quantization Dynamic Fixed-Point Number[1] 16-bit Fixed-Point Number [2] XNOR-Nets[3] BinaryNet

fig 2: Representative methods for CNN using low precision high speed rate

Compression of model and speedup by SIMD instruction

CNN with low precision arithmetic



#### float16

• Data type : float16 -> Halves memory consumption Computation type : SIMD instruction using float16 -> Doubled throughput



#### Experiment

fig6 : im2col /

convolution lowering[5]

### Speed comparison of matrix multiply-add

#### product-sum calculation of the matrix



fp32, int8 : NVIDIA GTX1080TI



4096 8192 16384 fig5 : Execution time comparison of multiply-accumulate operation of matrix (m×n×k matrixsize=m=n=k)

## Influence of low-precision data-type on CNN recognition performance

	51.1% <b>Q</b>	70.91%	Precision	Description	
top1-accuracy	50.825%	70.898%	float32	Data type : fp32(single precision) Computation type : fp32(single precision)	
	50.55%	70.885%	float16	Data type : float16 Computation type : SIMD instruction using float16	
	50.275% • accuracy-AlexNet 50% • accuracy-VGG16	70.8/3%	int8	Data type : int8 quantization (minmax / absmax) Computation type : float32 ( SIMD instruction using float16 int8 / future work)	
fiç	is considered to be the effect of noise by application of low precision type[6] lexNet, VGG16				

#### impact and speedup of half-precision computation / data-type application on CNN



### Conclusion

purpose of this research

#### Future work

Validation and evaluation of a method for **compressing** the amount of **data** contained in convolution neural network using low precision data type and low precision arithmetic and speeding up by SIMD instruction

#### conclusion

- Application of half-precision arithmetic is sufficiently effective even in the data type in the layer which is memory bound (speed up of about 1.78x, memory compression <sup>2</sup>2x)
- Speeding up in the SIMD instruction of float16 is effective for the computation bound layer, but it is about 1.58x at maximum
- The accuracy of recognition does NOT degrade depending on CNN and the method of inference using int 8 data type

Computation	Inference	GPU	Memory	top-1	top-5
Precision	Time	UseRate	Usage	acc	acc
fp32	66.785ms	99%	12845MiB	0.56828	0.79950
Dfp16Mfp32	60.065ms	99%	7475MiB	0.56813	0.79962
Dfp16Mfp16	51.005ms	99%	7475MiB	0.56821	0.79944

fig10: Acceleration of AlexNet by half precision data type and application of half precision arithmetic and its effect on recognition accuracy

- Verification of half precision arithmetic using NVIDIA TESLA V100 GPU implementation of int8 arithmetic type corresponding to 8-bit quantization
- Verification of convergence of learning by 8-bit quantization Validation of effective use of weights of compressed models such as
- Deep Compression[7]



Future work

[1] S. Gupta, A. Agrawal, K. Gopalakrishnan, P. Narayanan, Deep Learning with Limited Numerical Precision., International Conference on Machine Learning, 2015 [2] M. Courbariaux, Y. Bengio, J. David, Training deep neural networks with low precision multiplications., Advances in Neural Information Processing Systems 28, 2015 [3] M. Rastegari, V. Ordonez, J. Redmon, A. Farhadi, XNOR-Net: ImageNet Classification Using Binary Convolutional Neural Networks., European Conference on Computer Vision, 2016 [4] M. Courbariaux, I. Hubara, D. Soudry, R. El-Yaniv, Y. Bengio, BinaryNet: Training Deep Neural Networks with Weights and Activations Constrained to +1 or -1., Computer Vision and Pattern Recognition, 2016 [5] K. Chellapilla, S. Puri, P. Simard, High Performance Convolutional Neural Networks for Document Processing, Tenth International Workshop on Frontiers in Handwriting Recognition, pp.~386-408, 2006 [6] Y Luo, F Yang, Deep Learning With Noise, http://www.andrew.cmu.edu/user/fanyang1/ deep-learning-with-noise.pdf, 2014 [7] S. Han, H. Mao, W. Dally, Deep Compression: Compressing Deep Neural Networks with Pruning, Trained Quantization and Huffman Coding, International Conference on Learning Representation, pp. 74-76, 2016