# Performance Evaluation of the Mixed Precision GMRES(m) Method using FP64 and FP32

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## 1 INTRODUCTION

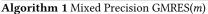
Traditionally, FLOPS (Floating-point Operations Per Second) for double-precision floating-point number (FP64) operation has been used as a metric for the performance of a computer system. However, it is getting difficult to improve FP64 FLOPS of a single processor due to the limitation of power budget. Besides, some new applications gradually accept low precision computing.

Under this circumstances, it is necessary to develop a new method that can exploit low precision computing and provide computed results with the same accuracy as by traditional methods using only FP64. The key idea here is mixed precision computing, in which both FP64 and low precision computing (e.g., FP32) are combined.

In this paper, the target problem is a system of linear equations Ax = b, where  $A \in \mathbb{R}^n$  is large and sparse. For this problem, a mixed precision variant of the GMRES(*m*) method using FP64 and FP32 (MP-GMRES(*m*)) is discussed, and its numerical behaviour is investigated in detail through numerical experiments.

#### 2 MIXED PRECISION GMRES(m) METHODS

The mixed precision variant of the GMRES(m) method (Algorithm 1) has the mathematical background related to the iterative refinement scheme for the solution of linear system ; this relation is clearly explained in the study by Imakura et al. [3] The effectiveness of MP-GMRES(m) has been confirmed in some cases [1, 4], however, its numerical behaviour is sill not clear.



**Require:**  $x_0$ : initial guess,  $\epsilon$ : convergence criterion, A; coefficient matrix, b: righthand side vector, maximum number of iterations 1:  $A^{(L)} = Low(A)$   $\triangleright$  precision converts from *standard* to *low* 

2: repeat 3:  $r_0 = b - Ax_0, \beta = ||r_0||_2$ 

- 4: if  $\beta / \|b\|_2 \le \epsilon$  then return  $x_0$
- 5:  $v_0 = r_0 / \beta$
- 6:  $\beta^{(L)} = Low(\beta), v_0^{(L)} = Low(v_0)$
- 7: Compute  $V_m^{(L)}$  and  $\tilde{H}_m^{(L)}$  by the *m*-step Arnoldi process with  $A^{(L)}$  and  $v_0^{(L)}$ .  $\triangleright$  using *low precision* arithmetic
- 8: Compute  $y_m^{(L)}$  from  $\beta^{(L)}$  and  $\tilde{H}_m^{(L)}$ .  $\triangleright$  using low precision arithmetic
- 9:  $z_m^{(L)} = V_m^{(L)} y_m^{(L)}$   $\triangleright$  using low precision arithmetic 10:  $z_m = \operatorname{Std}(z_m^{(L)})$   $\triangleright$  precision converts from low to standard

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z_m = \operatorname{Std}(z_m^{(\mathrm{L})})x_0 = x_0 + z_m
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11: x_0 = x_0 + z_m
12: until attain the maximum number of iterations
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Ensure: x_0
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### **3 NUMERICAL EXPERIMENTS**

In order to clarify the numerical behaviour of MP-GMRES(m), numerical experiments with the matrices obtained from the SuiteS-parse Matrix Collection [2] are conducted, in which MP-GMRES(m)



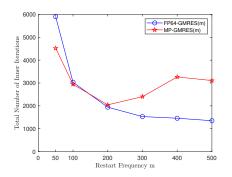


Figure 1: The change of the number of the total inner iterations in FP64-GMRES(m) and MP-GMRES(m) for memplus.

is compared with conventional GMRES(m) using only FP64 (FP64-GMRES(m)). The number of iterations, the achievable accuracy and the execution time are investigated. The detail of the numerical results will be presented in the poster.

In terms of the number of iterations, when m is small, the number of iterations of the two methods are almost equal. However, when m becomes larger, the number of iterations of FP64-GMRES(m) basically decreases, but that of MP-GMRES(m) tends to increase. Take matrix named memplus for example, Figure 1 illustrates the change of the number of the total inner iterations with m.

For the achievable accuracy, both FP64-GMRES(*m*) and MP-GMRES(*m*) attained the convergence criterion  $(||b - Ax||_2/||b||_2 \le 10^{-10})$  in most cases. If both two methods did not converge, there is almost no difference in the final accuracy.

When *m* is small, the execution time of both methods tends to be short. For many test matrices, when the number of iterations is almost equal, MP-GMRES(*m*) outperforms FP64-GMRES(*m*) in terms of the execution time. Even in the case that larger *m* reduces the number of iterations in FP64-GMRES(*m*), e.g., matrix named wang4, MP-GMRES(*m*) is still faster, in which about three times the iterations are required.

#### REFERENCES

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