# **Experimenting with GPTune for Optimizing** Linear Algebra Computations

#### Makoto Morishita<sup>1</sup>, Osni Marques<sup>2</sup>, Yang Liu<sup>2</sup>, Takahiro Katagiri<sup>1</sup> <sup>1</sup>Nagoya University, <sup>2</sup>Lawrence Berkeley National Laboratory

## ABSTRACT

In High Performance Computing (HPC), software often has many parameters that impact its performance. However, it is difficult to determine optimal values for such parameters in an impromptu way. The automatic tuning – of parameters is therefore an area of great interest. The purpose of this work is to understand the methodology of GPTune, which is an autotuning framework developed by DOE's Exascale Computing Project, and use the framework in a set of applications of interest.

### GPTune

GPTune is an autotuning framework that solves an underlying black-box optimization problem, using surrogate modeling. GPTune uses Bayesian optimization based on Gaussian Process regression and supports advanced features such as multi-task learning, transfer learning, multi-fidelity/objective tuning, and parameter sensitivity analysis. GPTune targets the autotuning of HPC codes, in particular applications that are very expensive to evaluate.

## EXPERIMENTS

In this work, we have focused on the autotuning of two algorithms implemented in ScaLAPACK: QR and LU factorizations. The former is provided with the GPTune repository, while the latter was implemented during my visit. In these cases, the tuning parameters are the block size (for cache memory optimization), and the process grid size for distributed computing.

#### Problem description

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- 1. Input space: This space defines the problems to be tuned. Every point in this space represents one instance of a problem.
- 2. Parameter Space: This space defines the application parameters to be tuned. A point in this space represents a combination of the parameters. The tuner finds the best possible combination of parameters that minimizes the objective function associated with the application.

parameter\_space = Space([Real(0., 1., transform="normalize", name="x")])

3. Output Space. This space defines the objective of the application to be optimized. For example, this can be runtime, memory or energy consumption in HPC applications or prediction accuracy in machine learning applications.

$5 \times 5$ matrix partitioned in $2 \times 2$ block size						2×2 process grid			
$\begin{bmatrix} a_{11} \\ a_{21} \end{bmatrix}$	$a_{12} \\ a_{22}$	$\begin{matrix} a_{13} \\ a_{23} \end{matrix}$	$\begin{array}{c} a_{14} \\ a_{24} \end{array}$	$a_{15} \\ a_{25}$		$\begin{bmatrix} a_{11} \\ a_{21} \end{bmatrix}$	<i>a</i> <sub>12</sub> 02	a <sub>15</sub> a <sub>25</sub>	$a_{13} a_{14} a_{24} a_{24}$
$\begin{bmatrix} a_{31} \\ a_{41} \\ \hline a_{51} \end{bmatrix}$	$a_{32} \\ a_{42} \\ a_{52}$	$a_{33}$ $a_{43}$ $a_{53}$	$a_{34} = \frac{a_{44}}{a_{54}} = \frac{a_{44}}{a_{54}}$	$\begin{array}{c}a_{35}\\a_{45}\\a_{55}\end{array}$	$\rightarrow$	$a_{51} \\ a_{31} \\ a_{41}$	$a_{52}$	a <sub>55</sub> a <sub>35</sub> a <sub>45</sub>	$ \begin{array}{c}   a_{53} & a_{54} \\   a_3 & a_{34} \\   a_4 & a_{44} \end{array} $

Figure 1. Example of 2D block-cyclic distribution

## HARDWARE

Environment	Name	OS	CPU		Memory
(proxy for a distributed environment)	MacBook Air (M1, 2020)	Ventura 13.4.1	Apple M1 chip 8 cores	)	8 [GB]
	Comp	utation	Tuning Parameters		
	QR factorization $A =$	• QR	nb	row	block size

#### **Objective Function**

The user need to define a (Python) function representing the objective function to be optimized

#### def objectives(point): x = point['x']# call HPC code with parameter x . . .

# get the function value f

• • •

return [f]

#### $A: A \in \mathbb{R}^{m \times n}$ column block size mbQ : orthogonal matrix *R* : upper triangular matrix number of processes pProblem number of MPI processes npernode *m=n=*1000 per compute node LU factorization row block size A = LUnb(= column block size) $A:A\in\mathbb{R}^{n\times n}$ *L* : lower triangular matrix *U* : upper triangular matrix row process grid $\mathcal{P}$ column process grid q

### **QR** factorization

## LU factorization



Figure 2. Execution time for 4 tuning parameters Left: (mb, nb), Right: (npernode, p). The optimal configuration is given by mb=88, nb=64, npernode=8, p=1 (blue arrow).

Figure 3. Execution time for 3 tuning parameters Left: (nb, p), Right: (nb, q). The optimal configuration is given by nb=32, p=1, q=3 (blue arrow).

### **FUTURE WORK**

 Install GPTune on the Supercomputer Flow Type I to examine its behavior on a Fugaku-type machine (arm architecture). Apply GPTune to applications of interest, including Fast Fourier Transform (e.g. FFTX, which is the exascale follow-on to the FFTW open source discrete FFT package).



Figure 4. Supercomputer Flow Type I (FUJITSU Supercomputer PRIMEHPC FX1000)

## DISCUSSION

(1) We have experimented with GPTune to find optimal parameters for QR and LU factorizations, therefore obtaining better performance. (2) We have learned how to select specific tuning parameters by changing the implementation of Space and Ojective Function in GPTune, and how to adapt the target application (in this case ScaLAPACK) to properly interface with GPTune.